

Algebraic circuit size lower bounds for restricted circuits, in a functional setting

Suryajith Chillara

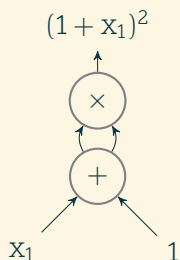


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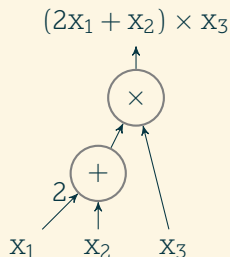
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Algebraic/Arithmetic circuits



An Arithmetic Circuit is a directed acyclic graph where

- leaf nodes: labelled by constants or variables,
- internal nodes: labelled by either \times or $+$,
- edges: labelled by constants.



Circuit size: number of nodes present in it.
[Measure of complexity]

Circuit depth: length of the longest leaf to root path.
[Measure of parallelizability]

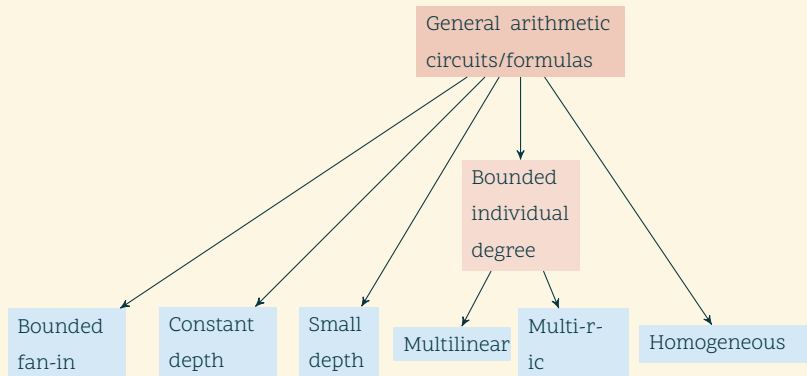
Formulas: circuits where computations are not reused, i.e., directed tree.

Best known general lower bounds

- ▶ Existential circuit size lower bound: $\Omega\left(\sqrt{\binom{N+d}{d}}\right)$
[Folklore].
- ▶ Explicit circuit size lower bound: $\Omega(N \log N)$ [Baur and Strassen, TCS 1983].
- ▶ Explicit formula size lower bound: $\Omega(N^2)$ [Kalorkoti, SICOMP 1985].

Circuit size lower bounds are known for restricted arithmetic circuits.

Simplifications considered



Functional lower bounds

Functionally equivalent (denoted by \equiv_{fn}^B)

$$P \equiv_{\text{fn}}^B Q \quad \text{if} \quad P(a) = Q(a) \quad \forall a \in B^{|\mathcal{X}|}.$$

Functional Lower Bounds

The evaluation table (over B^N) of any circuit in \mathcal{C} of size at most s , is not equal to that of P .

Further, if $P \equiv_{\text{fn}}^B Q$ then

$$P \notin_{\text{fn}} \text{ASIZE}(s) \quad \implies \quad Q \notin_{\text{fn}} \text{ASIZE}(s).$$

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- ▶ Restricted depth four and depth three circuits [Forbes, Kumar and Saptharishi, CCC 2016].

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Boolean part of a class \mathcal{C}

For a circuit $C \in \mathcal{C}$, let $\mathbf{BP}(C)$ be the boolean circuit that simulates the evaluation of C over $\{0, 1\}^N$.

$$\mathbf{BP}(\mathcal{C}) = \{\mathbf{BP}(C) \mid C \in \mathcal{C}\}.$$

Path to boolean lower bounds

Theorem [Bürgisser, TCS 2000]

1. (GRH) Over large fields,

– $\text{FNC}^1/\text{poly} \subseteq \text{BP}(\text{VP}) \subseteq \text{FNC}^3/\text{poly}$ and

– $\#\text{P}/\text{poly} \subseteq \text{BP}(\text{VNP}) \subseteq \text{FP}^{\#\text{P}}/\text{poly}$

2. For fixed size finite fields,

– $\text{FNC}^1/\text{poly} \subseteq \text{BP}(\text{VP}) \subseteq \text{FNC}^2/\text{poly}$ and

– $\#\text{P}/\text{poly} = \text{BP}(\text{VNP})$

Constant depth Boolean circuits

ACC^0

Constant depth circuits with AND, OR, NOT and MOD gates.

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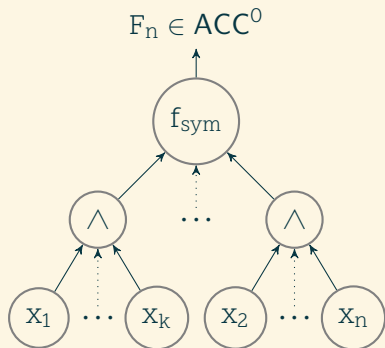
Theorem [Murray and Williams, SICOMP 2020]

$NQP \not\subseteq Non-uniform-ACC^0$.

Characterization for ACC^0

Theorem [Yao, FOCS 1985;
Beigel-Tarui, CC 1994]

Every language L in the class ACC^0 can be recognized by a family of depth two deterministic circuits with a symmetric function gate at the root and $2^{\log^{O(1)} n}$ many AND gates of fan-in $\log^{O(1)} n$.



Observation

Observation [Forbes, Kumar and Saptharishi, CCC 2016]

Over $\{0, 1\}^N$, any function F in ACC^0 can also be computed algebraically as follows.

$$F(X) = \sum_{i=1}^s (Q_i(X))^{d_i} .$$

where s and each d_i are at most $2^{\log^{O(1)} n}$. Further, monomials of Q_i 's are supported on at most $\log^{O(1)} n$ variables.

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We denote such expressions by $\Sigma\wedge\Sigma\Pi$.

An approach towards ACC^0 lower bounds

A strategy

Show that there exists a function F such that

- ▶ the evaluation table of $F \neq$ evaluation table of any “small” $\Sigma\wedge\Sigma\Pi$ expressions, and
- ▶ F is computable in a class that is not “much larger” than ACC^0 .

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Our result

There is a function F such that

- ▶ F is computable in GapL , and
- ▶ the evaluation table of F is not equal to the evaluation table of any “small” and “bounded individual degree” $\Sigma\wedge\Sigma\Pi$ expressions.

Our results

Main result

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This result is obtained by proving “functional” size lower bounds against restricted arithmetic circuits of depth four.

Bird's eye view of proof

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Step 1

There is an explicit polynomial P such that it is not functionally equivalent to polynomials of bounded individual degree that are computed by “small” $\Sigma\wedge\Sigma\Pi$ circuits.

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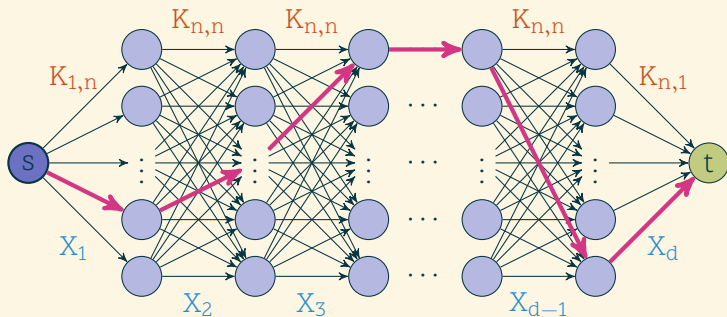
Step 1

There is an explicit polynomial P such that it is not functionally equivalent to polynomials of bounded individual degree that are computed by “small” $\Sigma\wedge\Sigma\Pi$ circuits.

Step 2

Show that there is a function $F \in \mathbf{GapL}$ that simulates the evaluation of P over $\{0, 1\}^N$.

Iterated Matrix Multiplication polynomial



$$\begin{aligned} \text{IMM}_{n,d} &= \sum_{(s \rightsquigarrow t) \text{ paths } \pi} \text{wt}(\pi) \\ &= \sum_{\pi_1, \dots, \pi_d \in [n]} X_{1, \pi_1}^{(1)} \cdot X_{\pi_1, \pi_2}^{(2)} \cdot \dots \cdot X_{\pi_{d-1}, 1}^{(d)} \end{aligned}$$

$\text{IMM}_{n,d}$ is the $(1, 1)$ entry in the product of adjacency matrices X_1, X_2, \dots, X_d .

$\{\text{IMM}_{n,d}\}_{n,d \geq 0} \in \text{VP}$ and has a depth four circuit of size $n^{O(\sqrt{d})}$.

Step 1: Broad theme of the proof

Define a suitable complexity measure $\Gamma : \mathbb{F}[X] \mapsto \mathbb{R}$ such that the following holds:

- For any polynomial f that is computed by a “small” circuit, $\Gamma(f)$ is “small”.
- For the target polynomial P , $\Gamma(P)$ is “large”.

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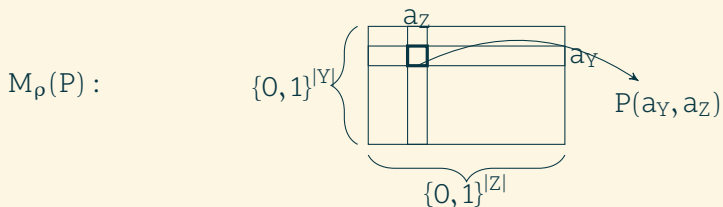
Multilinear Shifted Evaluation Dimension (denoted by $\text{mSED}_{k,\ell}^{[Y,Z]}(P(Y,Z))$)

$$\dim \left(\text{Eval}_{\{0,1\}^{|Z|}} \left\{ \text{mult} \left(Z^{\leq \ell} \cdot \mathbb{F}\text{-span} \left\{ P(a, Z) \mid a \in \{0,1\}_{\leq k}^{|Y|} \right\} \right) \right\} \right)$$

Based on the measure of Shifted Evaluation Dimension, of [Forbes, Kumar, and Saptharishi, CCC 2016]

Evaluation Dimension

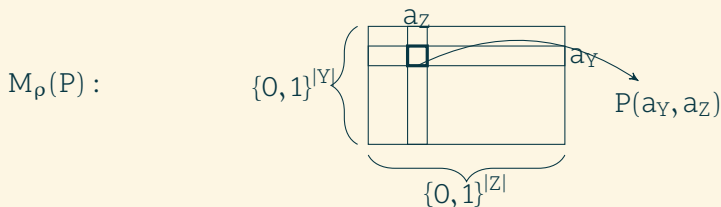
Let $\rho : X \mapsto Y \sqcup Z$ be a partitioning function.



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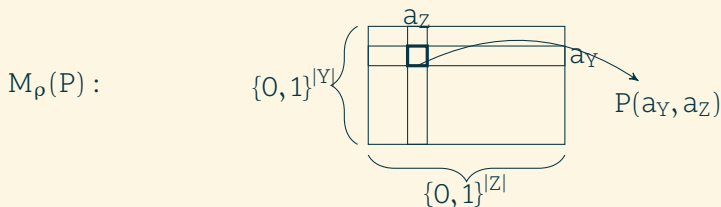
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$$\text{rank}(M_\rho(P)) = \dim \left(\text{Eval}_{\{0,1\}^{|Z|}} \left(\mathbb{F}\text{-span} \left\{ P(a, Z) \mid a \in \{0, 1\}^{|Y|} \right\} \right) \right).$$

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Let $\rho : X \mapsto Y \sqcup Z$ be a partitioning function.



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$$\text{rank}_{\leq k}(M_\rho(P)) = \dim \left(\text{Eval}_{\{0,1\}^{|Z|}} \left(\mathbb{F}\text{-span} \left\{ P(a, Z) \mid a \in \{0, 1\}^{|Y|}_{\leq k} \right\} \right) \right).$$

Partial derivatives as a proxy

- ▶ For a set-multilinear polynomial P and $a \in \{0, 1\}_{\leq k}^{|Y|}$,

$$\frac{\partial^k P}{\partial Y^a} = P(a, Z).$$

- ▶ For a polynomial Q of individual-degree at most r ,

$$\mathbb{F}\text{-span} \left\{ Q(a, Z) \mid a \in \{0, 1\}_{\leq k}^{|Y|} \right\} \subseteq \mathbb{F}\text{-span} \left\{ (\partial^{\leq r \cdot k} Q)|_{Y=0} \right\}.$$

Evolved measures

- ▶ Shifted Evaluation Dimension [Forbes, Kumar and Saptharishi, CCC 2016]:

$$\dim \left(\text{Eval}_{\{0,1\}^{|Z|}} \left\{ Z^{\ell} \cdot \mathbb{F}\text{-span} \left\{ P(a, Z) \mid a \in \{0,1\}_{\leq k}^{|Y|} \right\} \right\} \right)$$

- ▶ Multilinear Shifted Evaluation Dimension [Our work]:

$$\dim \left(\text{Eval}_{\{0,1\}^{|Z|}} \left\{ \text{mult} \left(Z^{\ell} \cdot \mathbb{F}\text{-span} \left\{ P(a, Z) \mid a \in \{0,1\}_{\leq k}^{|Y|} \right\} \right) \right\} \right)$$

Formal statement

Main Theorem

Let n be a large integer and d, k and r be such that

- ▶ $\omega(\log^2 n) \leq d \leq n^{0.01}$ and
- ▶ $r \leq \frac{d}{1201k^2}$.

Any depth four $\Sigma \wedge \Sigma \Pi$ circuit of bounded individual degree r computing a function equivalent to $\text{IMM}_{n,d}$ on $\{0, 1\}^{n^2 d}$, must have size at least $n^{\Omega(k)}$.

Step 2

Theorem [Vinay, CCC 1991]

Evaluation of $\text{IMM}_{n,d}$ over $\{0,1\}^{n^2d}$ can be simulated in GapL .

Our result

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“Improving” this result could lead us to a separation of ACC^0 from GapL .

Other results and related work

Circuit model	Work	Poly	Lower Bound	Range of parameters
$(\Sigma \wedge \Sigma \Pi)_{\leq r}$	This work	$\text{IMM}_{n,d}$	$n^{\Omega(k)}$	$\omega(\log^2 n) \leq d \leq n^{0.01}$, and $r \leq \frac{d}{1201k^2}$.
$(\Sigma \Pi \Sigma \Pi)_{\substack{\leq r \\ [\leq d]}}$	[FKS16]	$\text{NW}_{m,d}$	$2^{\Omega(\sqrt{d} \log(md))}$	$m = \Theta(d^2)$, and $r \leq O(1)$.
$(\Sigma \Pi \Sigma \Pi)_{\substack{\leq r \\ [\leq d]}}$	This work	$\text{IMM}_{n,d}$	$n^{\Omega(\sqrt{\frac{d}{r}})}$	$\omega(\log^2 n) \leq d \leq n^{0.01}$, and $r \leq \frac{\log n}{12}$.

[FKS16] = [Forbes, Kumar and Saptharishi, CCC 2016].

Further observations

At least one of the following is true.

- ▶ There exists a multilinear polynomial which is “hard” for $\Sigma\wedge\Sigma\Pi$ circuits but can be evaluated using a “small” $\Sigma\wedge\Sigma\Pi$ circuits.
- ▶ $\text{ACC}^0 \subsetneq \text{GapL}$.

Thank you!